

FIG. 1

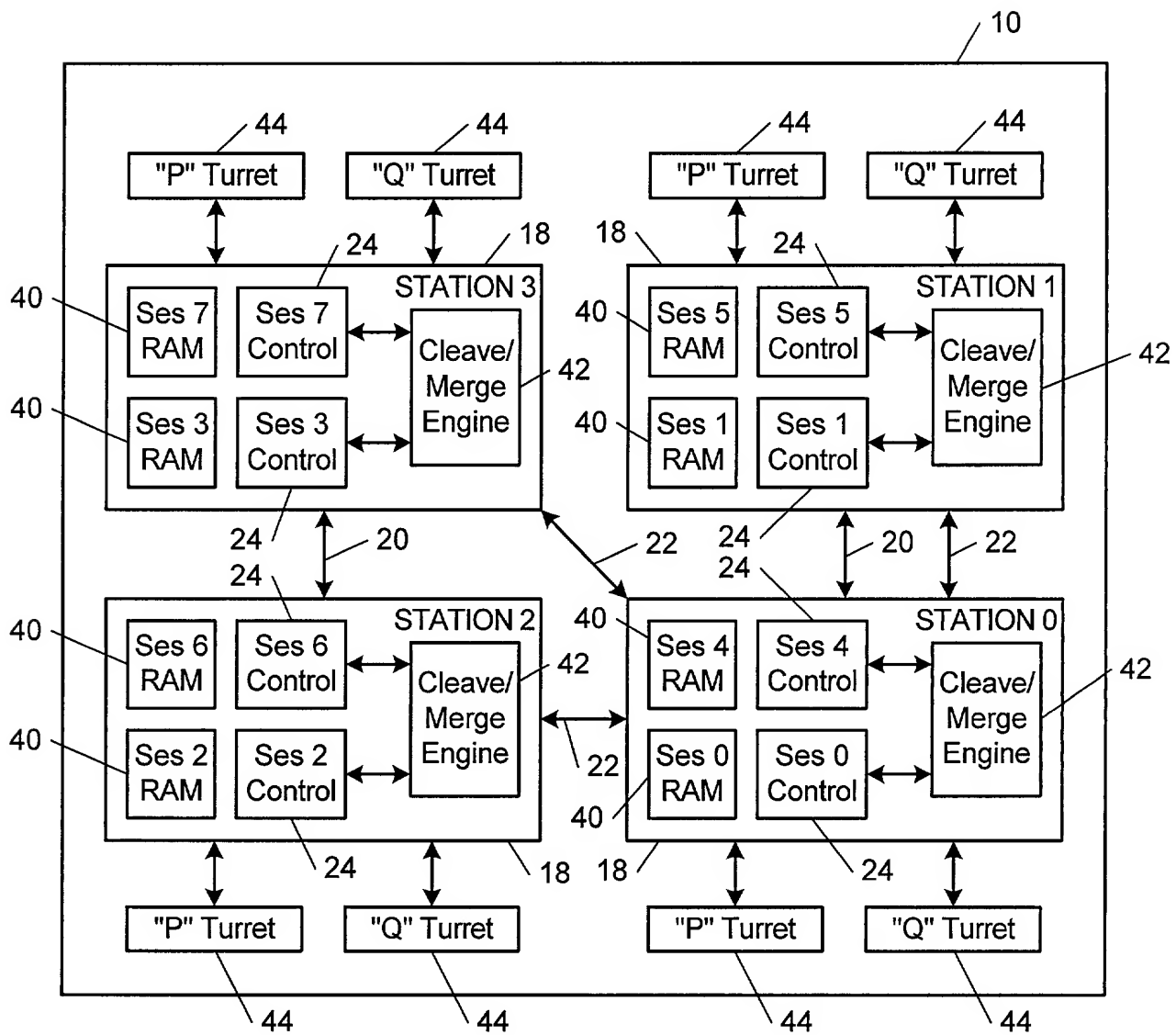


FIG. 2

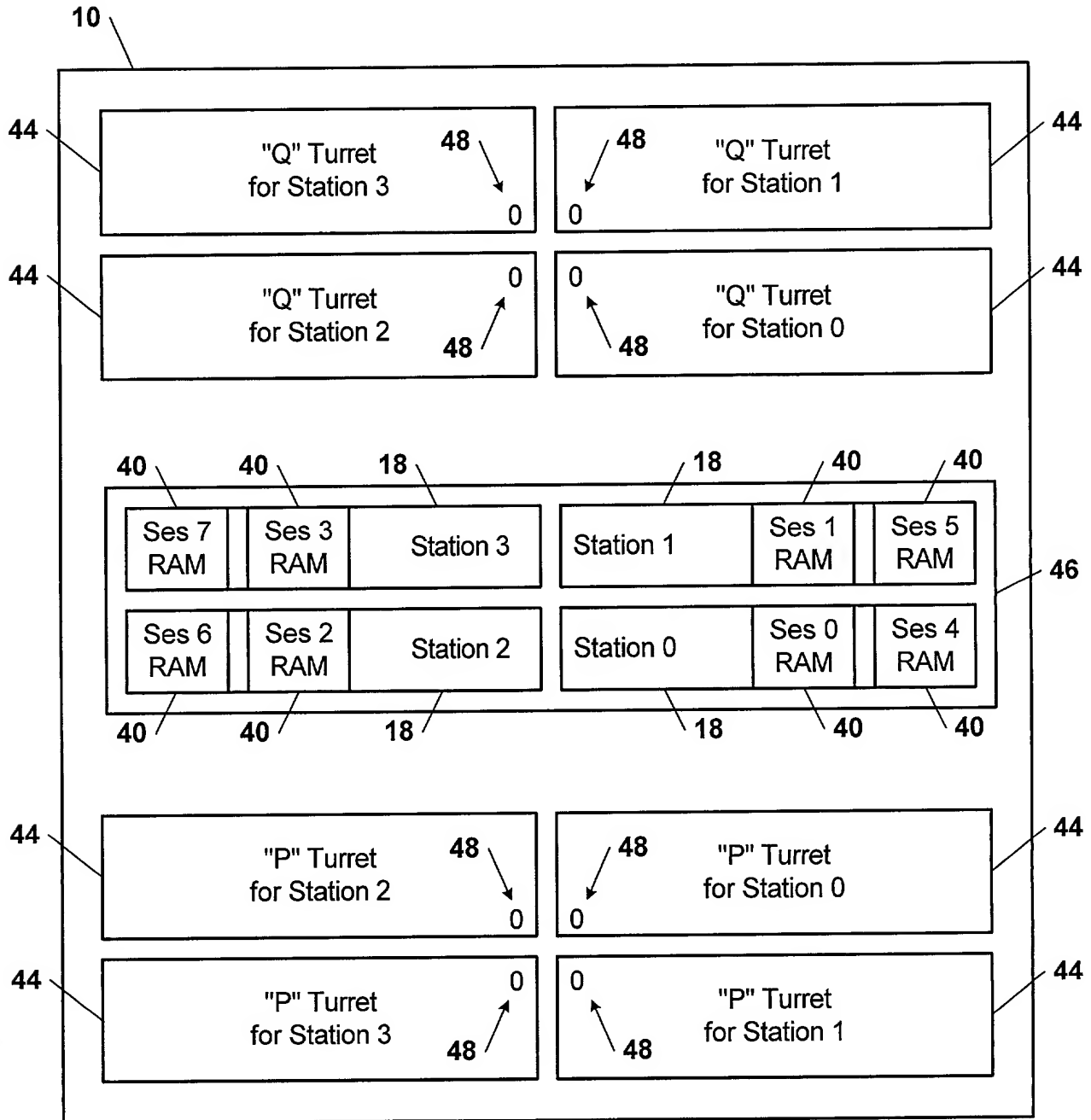


FIG. 3

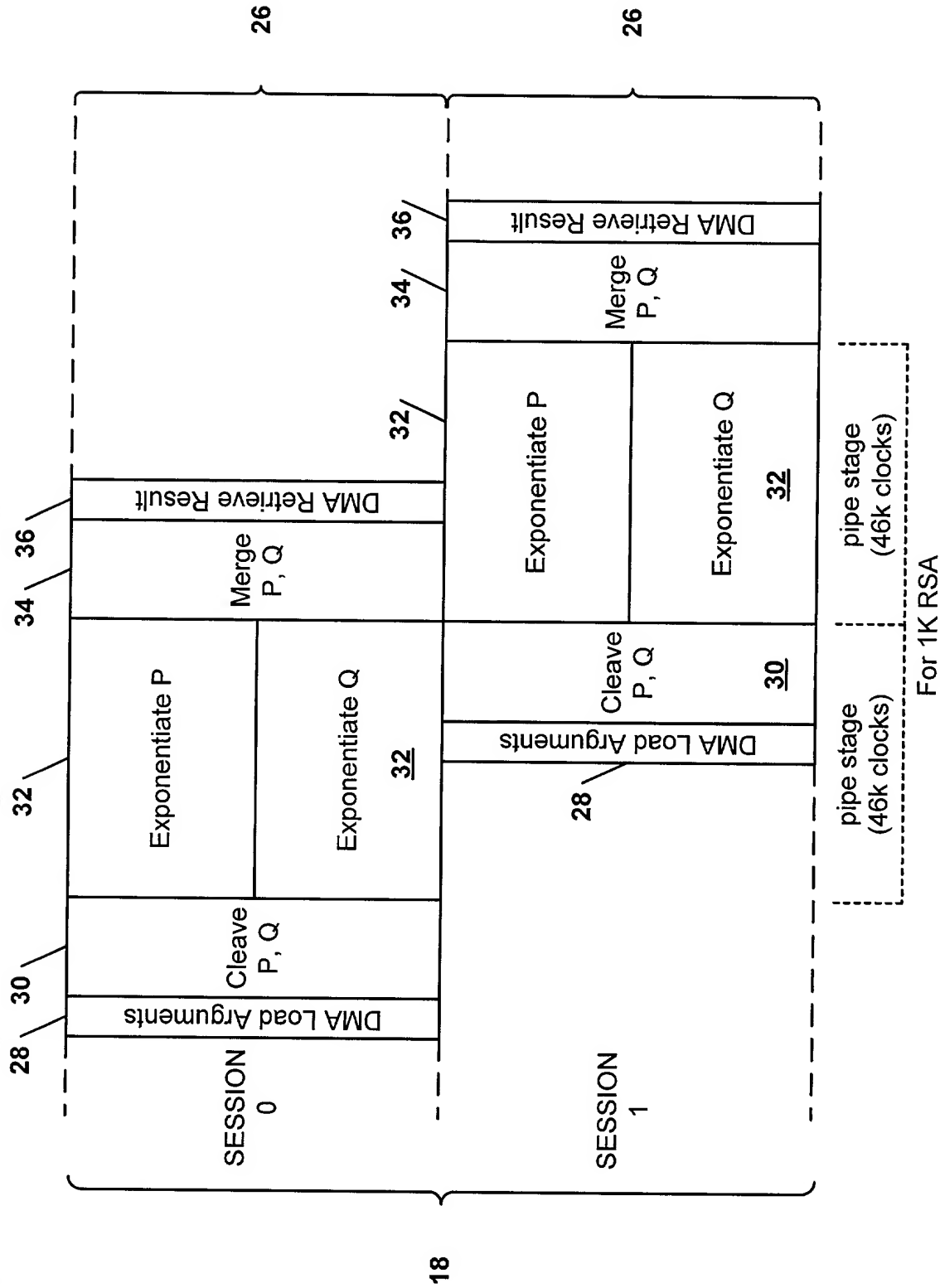
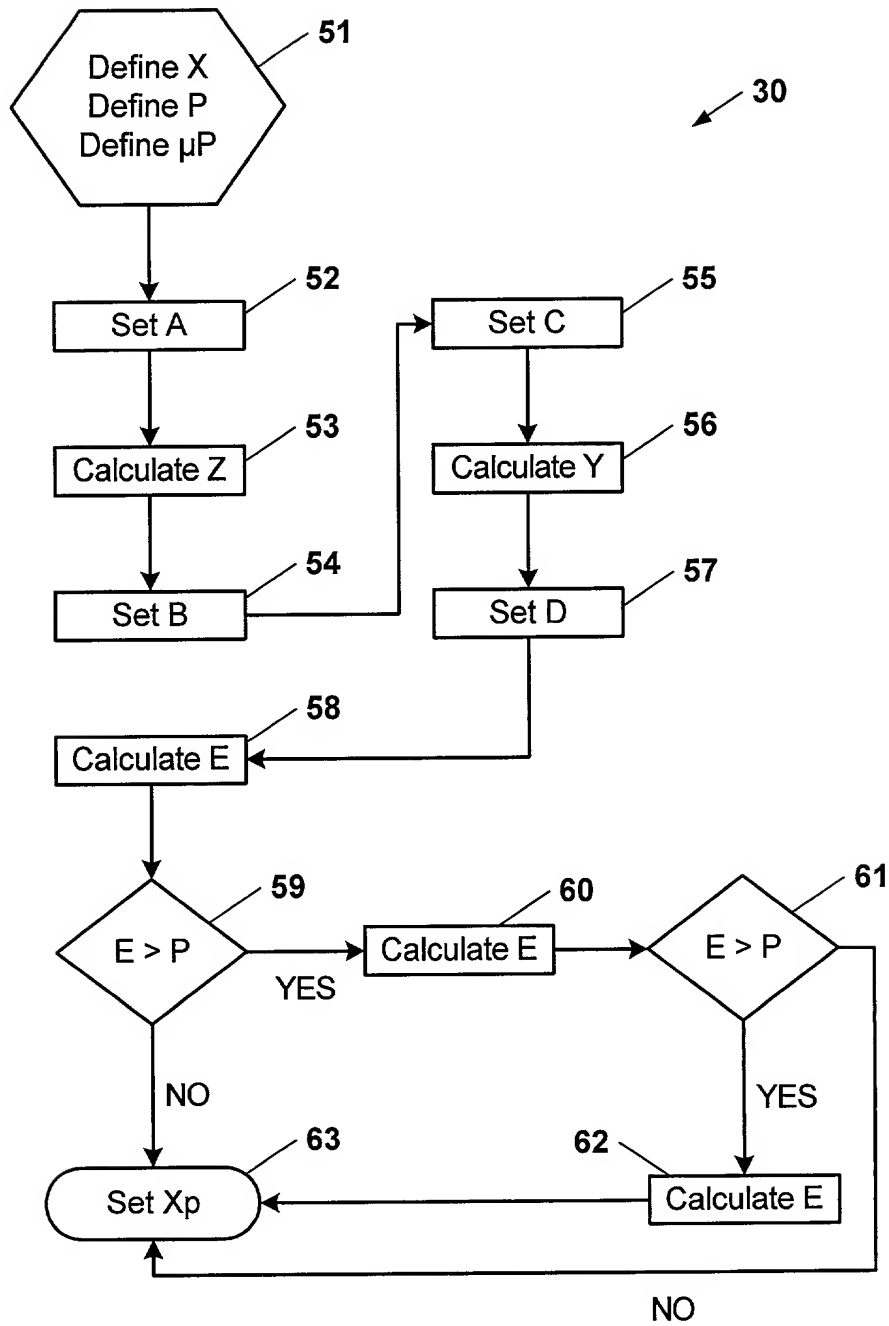
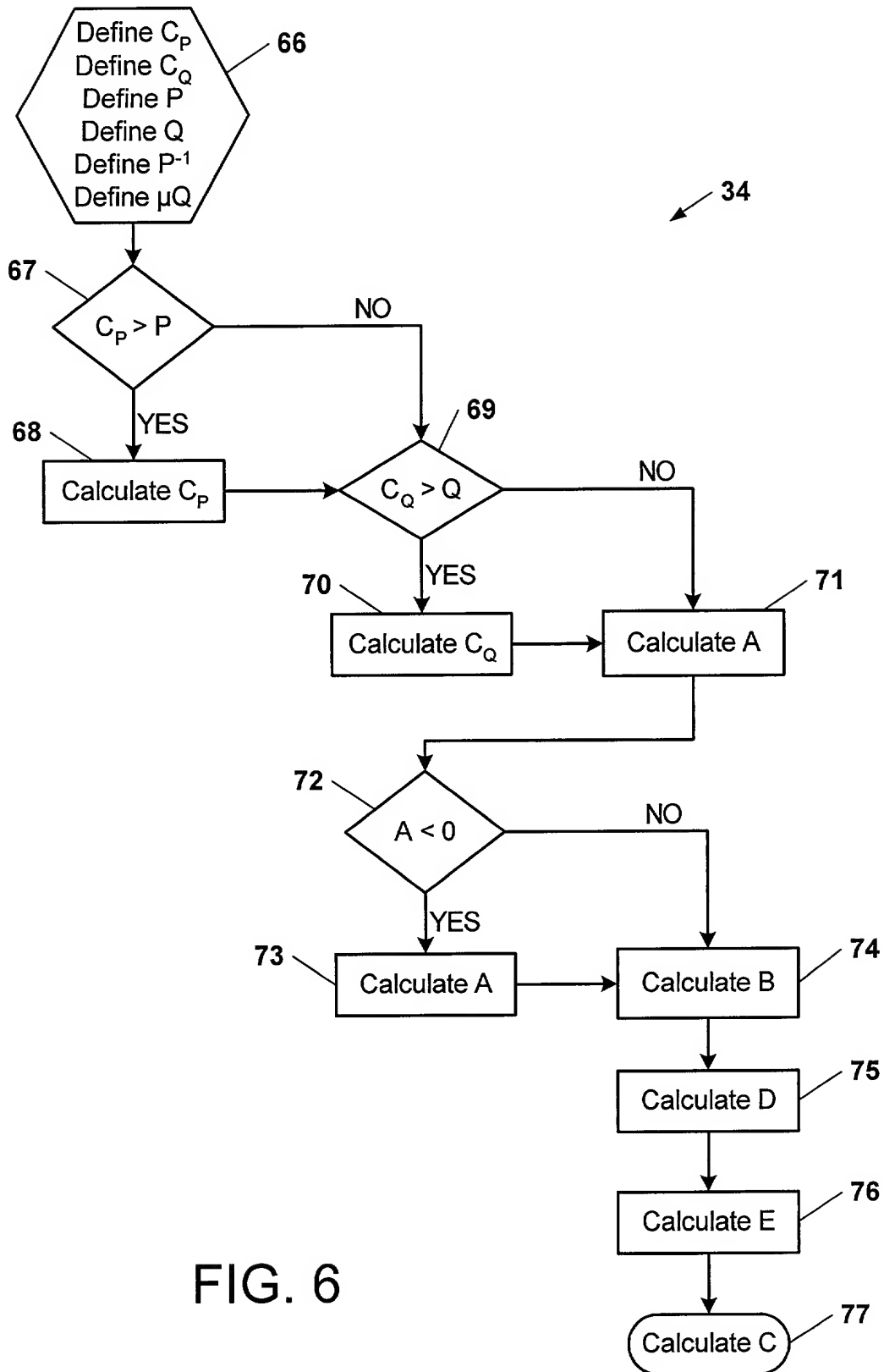


FIG. 4





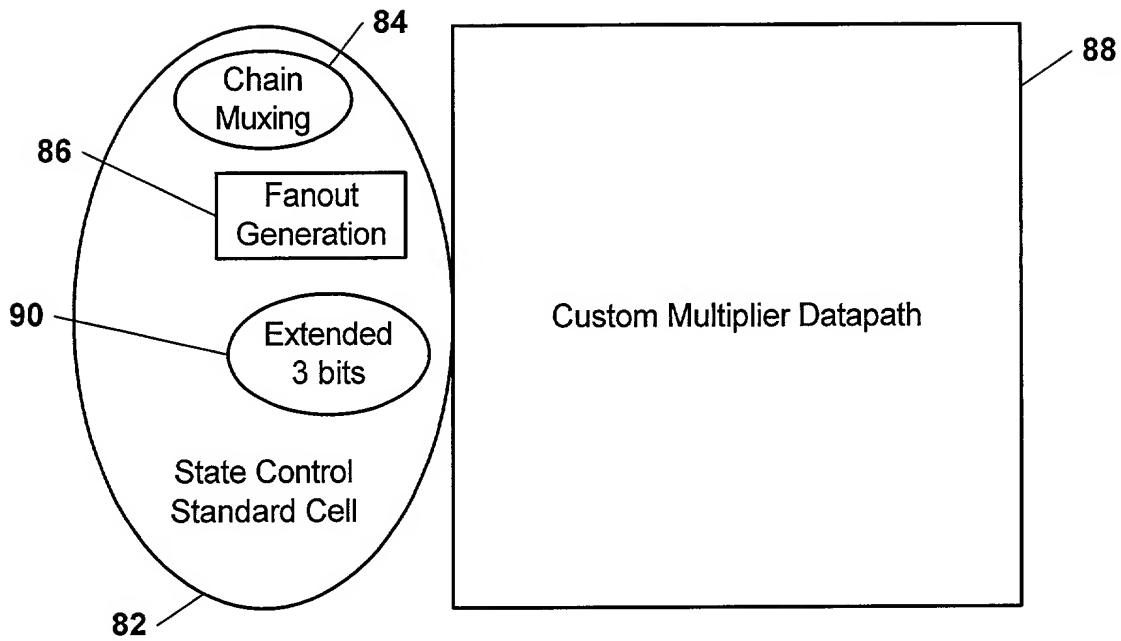


FIG. 7

Gated Clock, Fanout, and X drivers	0	63	88
	Dual 128 bit full adders	<u>92</u>	
	127	64	
	511	448	
	Dual 128 bit full adders	<u>92</u>	
	384	447	
	128	191	
	Dual 128 bit full adders	<u>92</u>	
	255	192	
	383	320	
	Dual 128 bit full adders	<u>92</u>	
	256	319	

FIG. 8

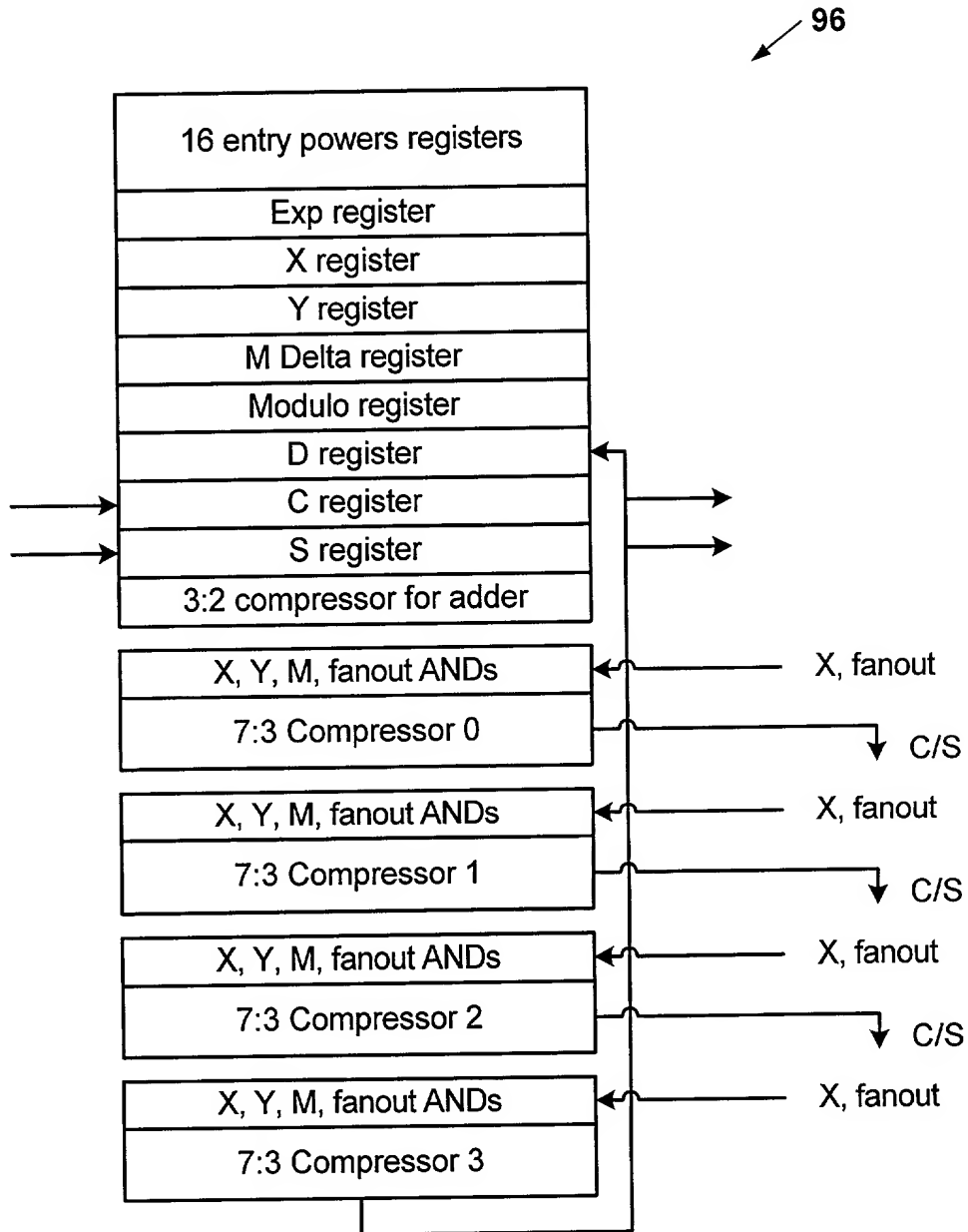


FIG. 9

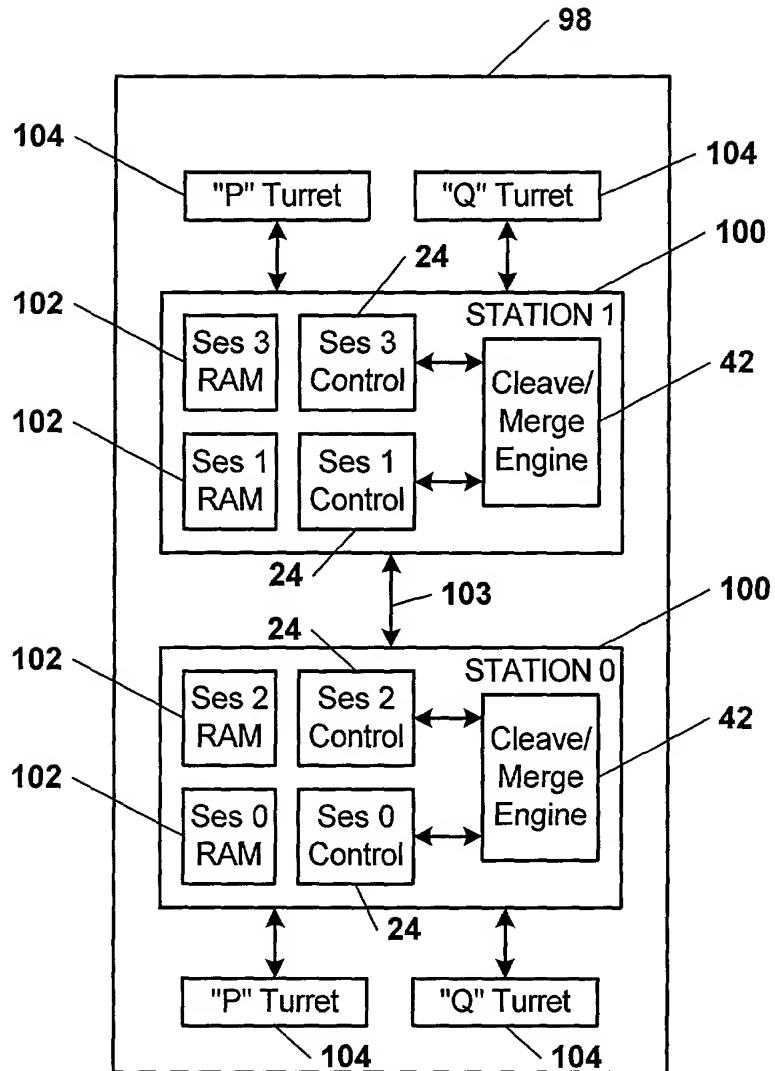


FIG. 10

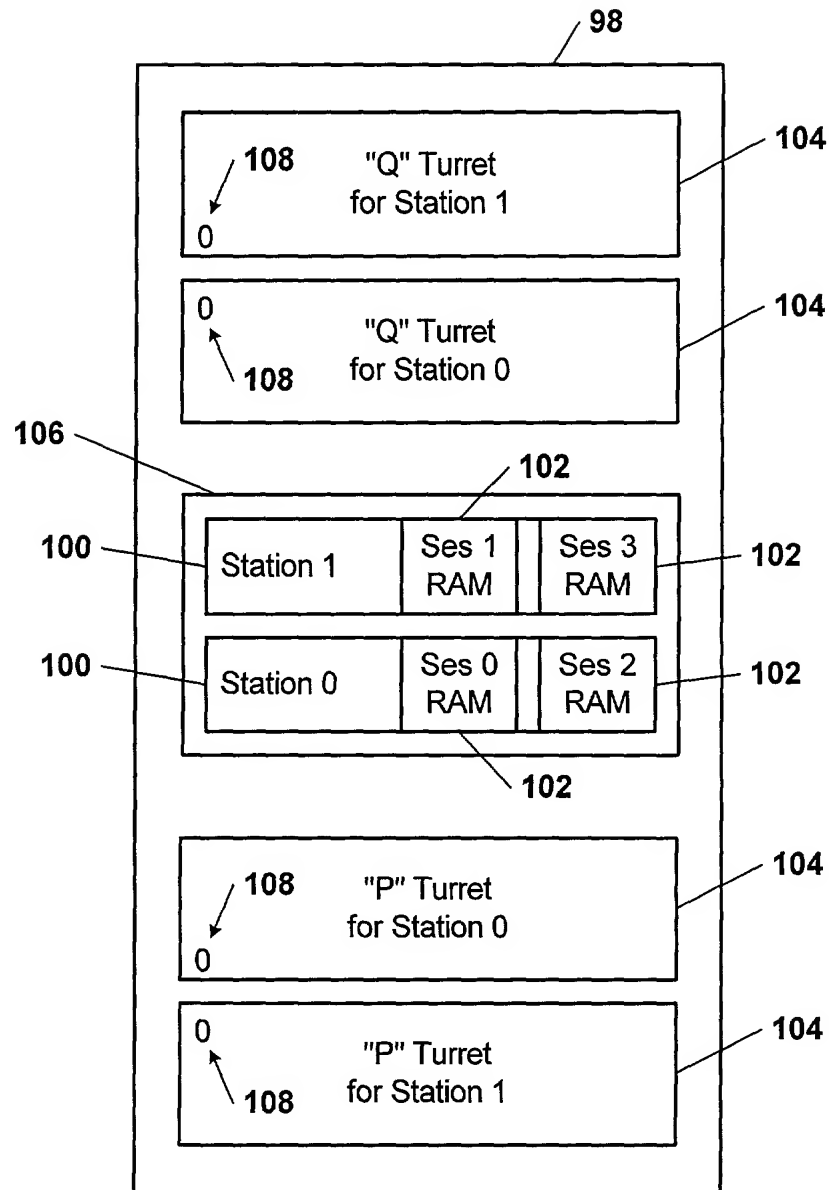


FIG. 11

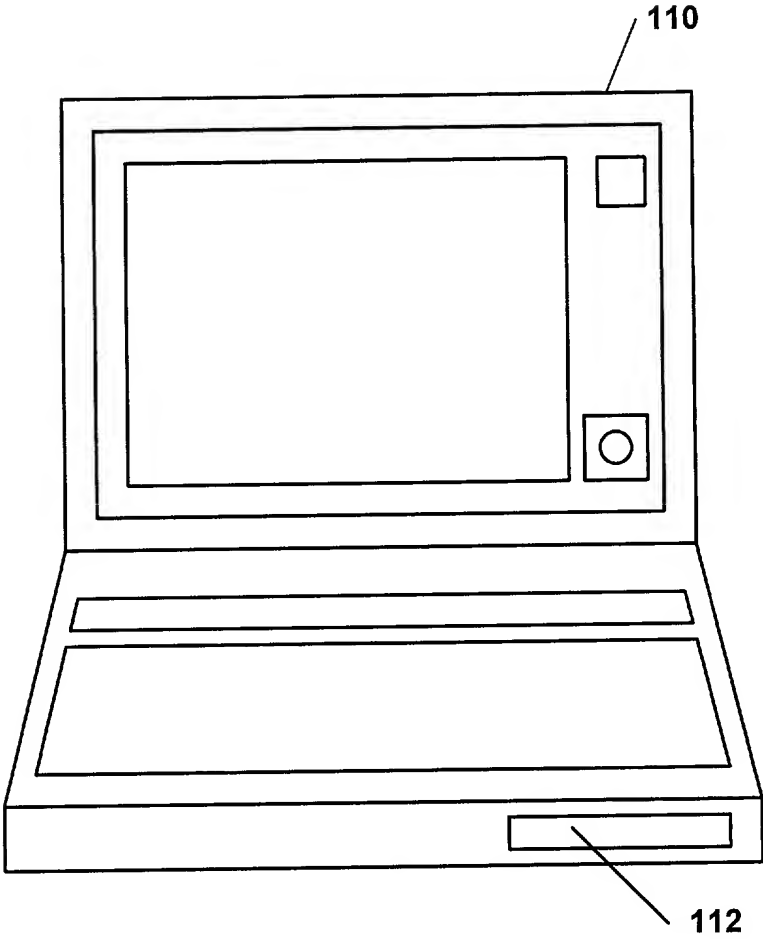


FIG. 12

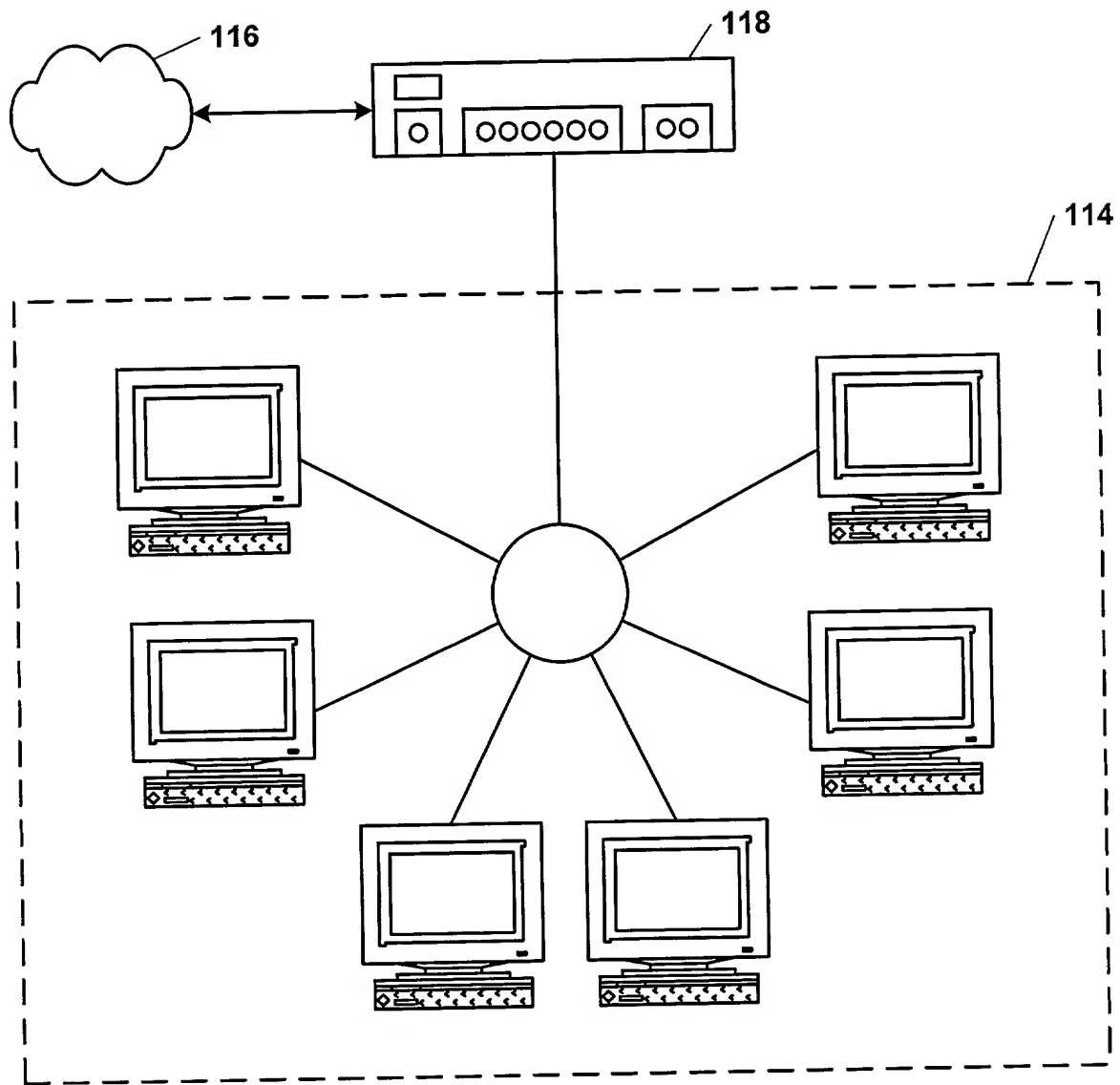


FIG. 13

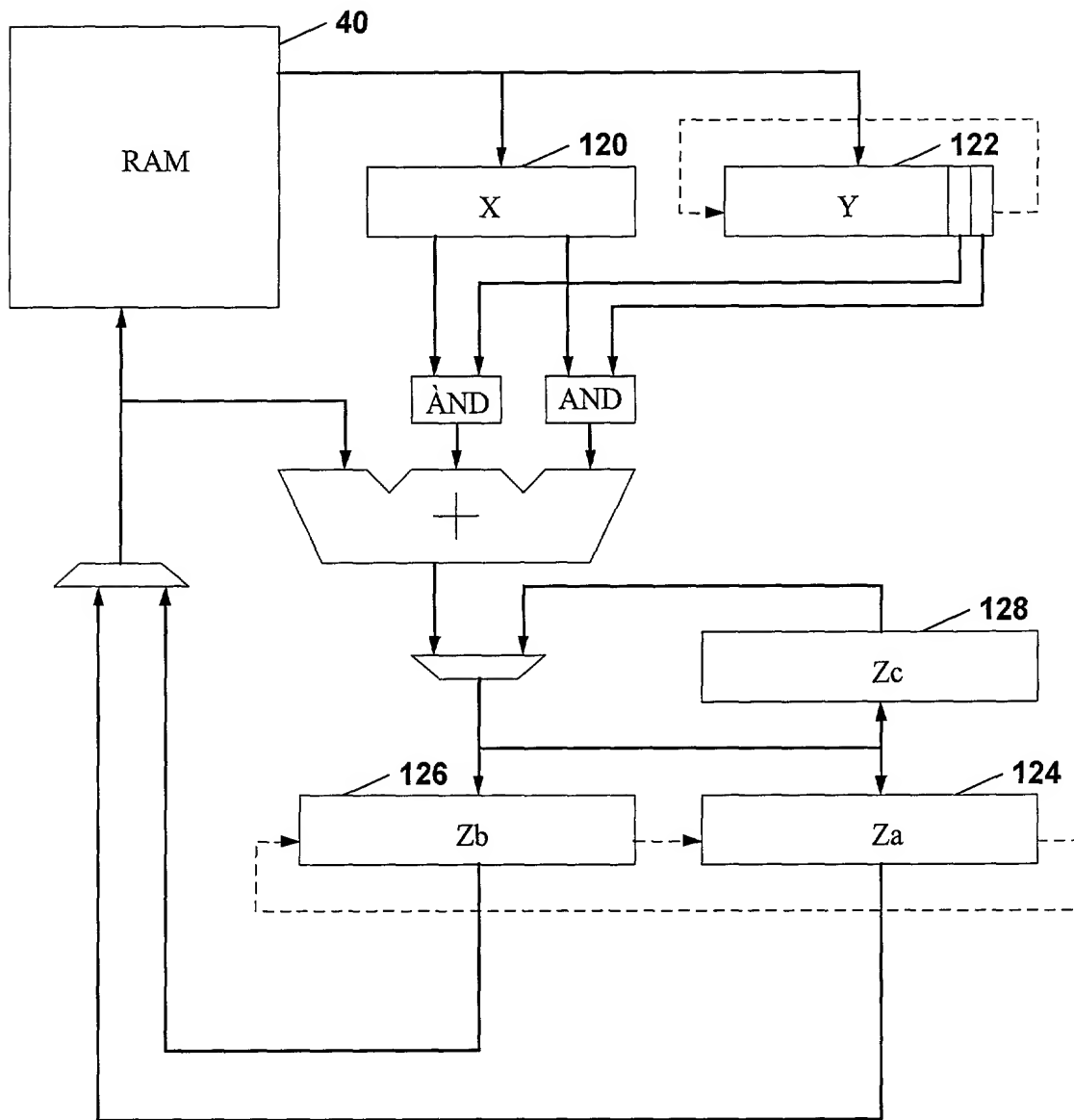


FIG. 14